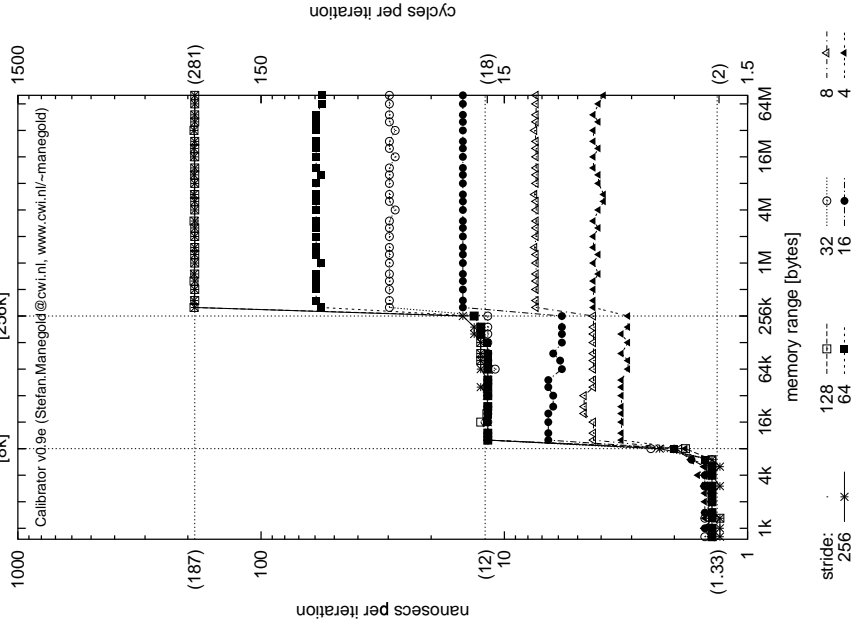
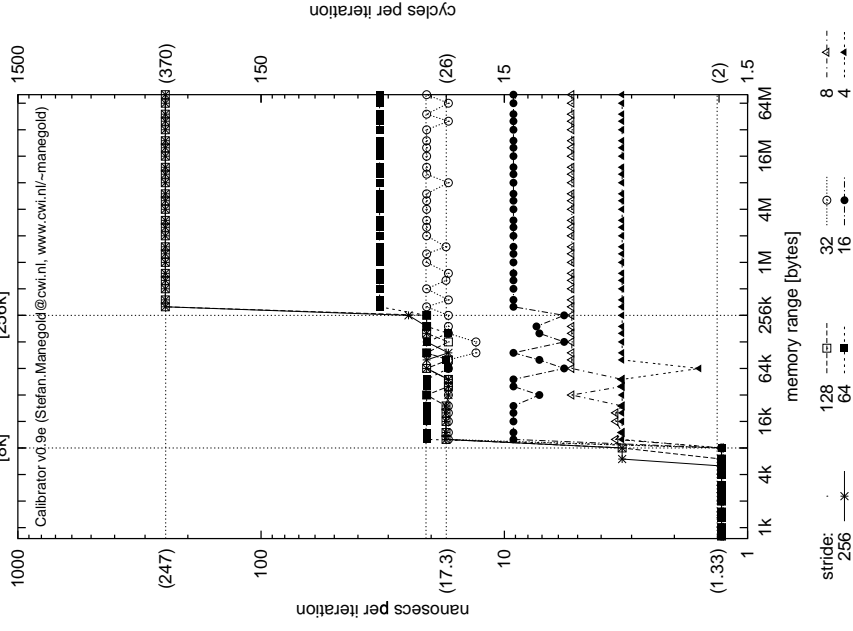


P4\_1500\_PC600\_Dual.Cache-replace-time



P4\_1500\_PC600\_Dual.Cache-miss-latency



P4\_1500\_PC600\_Dual.TLB-miss-latency

