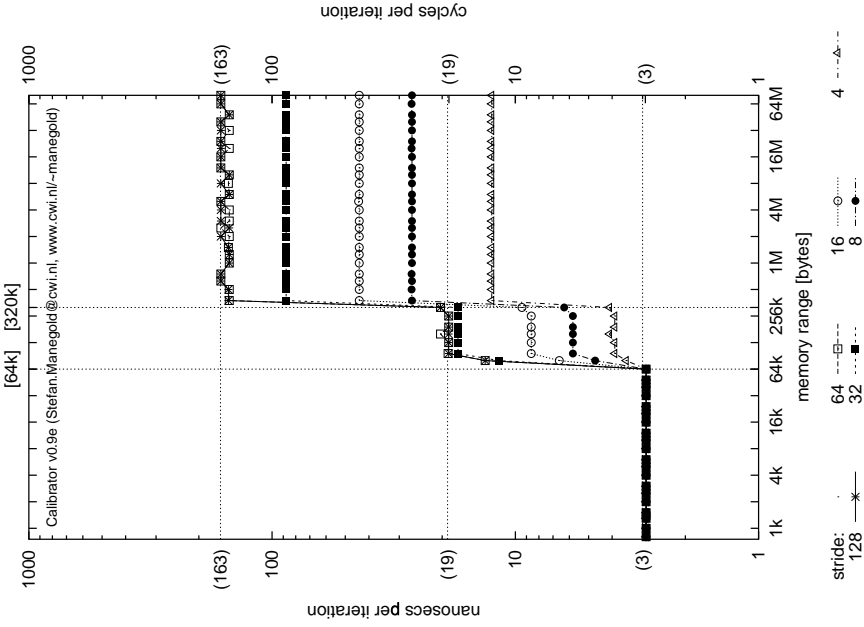
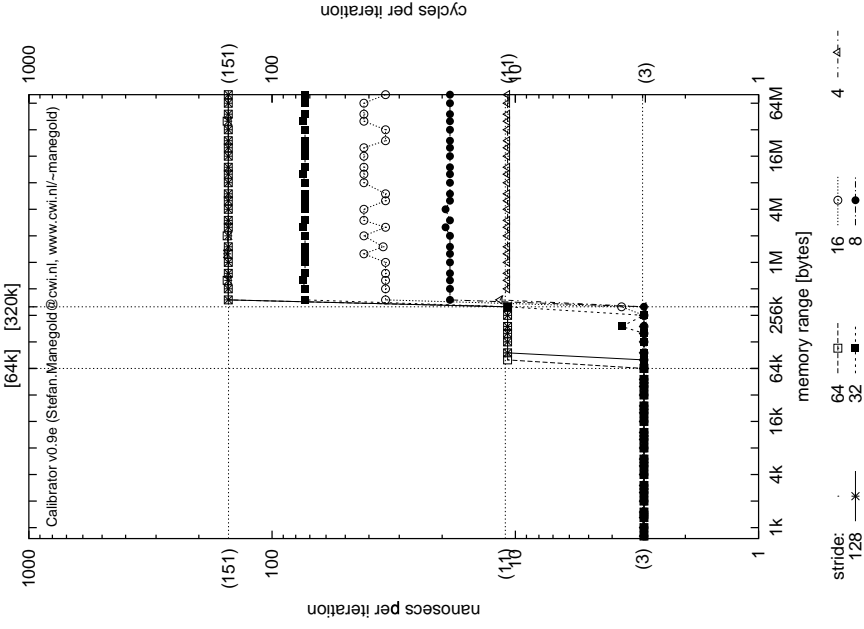


Athlon\_1000\_DDR\_PC1600.cache-replace-time



Athlon\_1000\_DDR\_PC1600.cache-miss-latency



Athlon\_1000\_DDR\_PC1600.TLB-miss-latency

