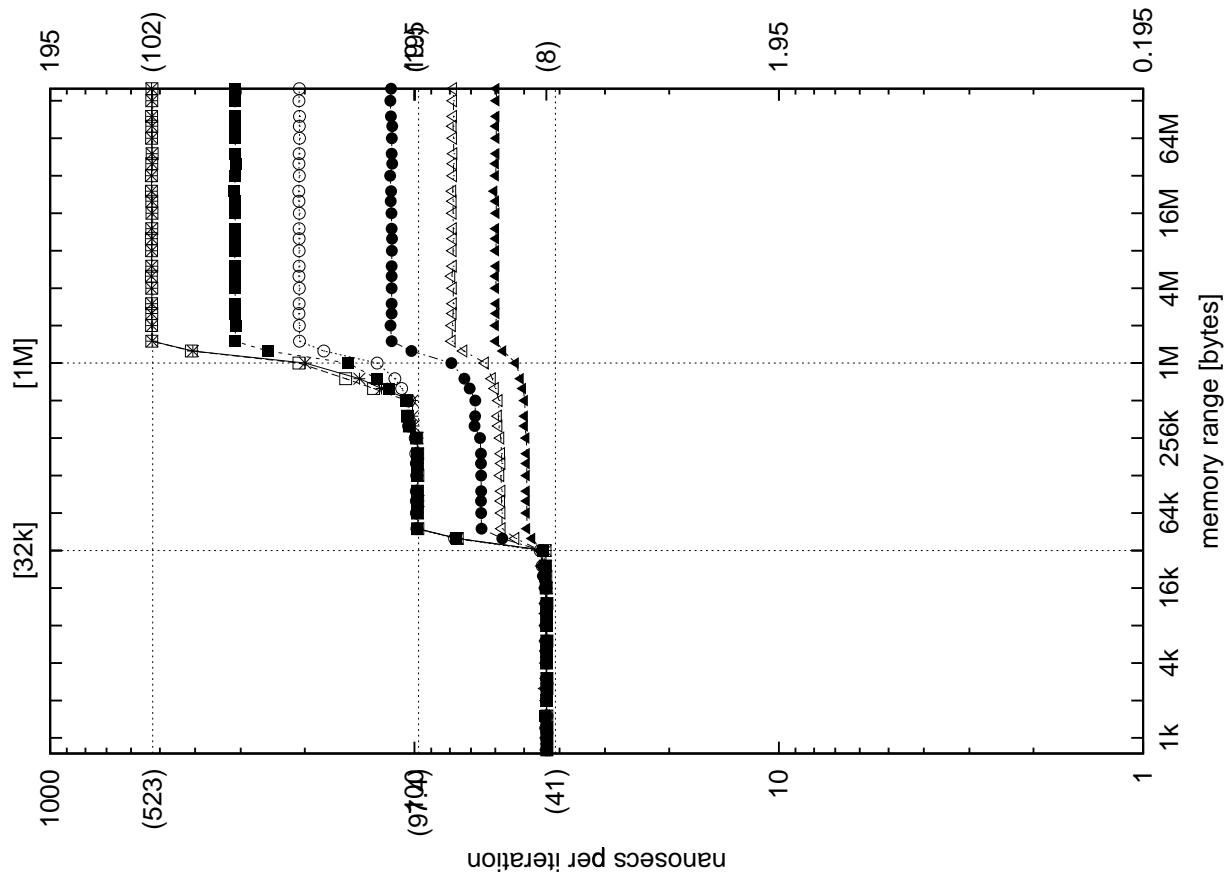
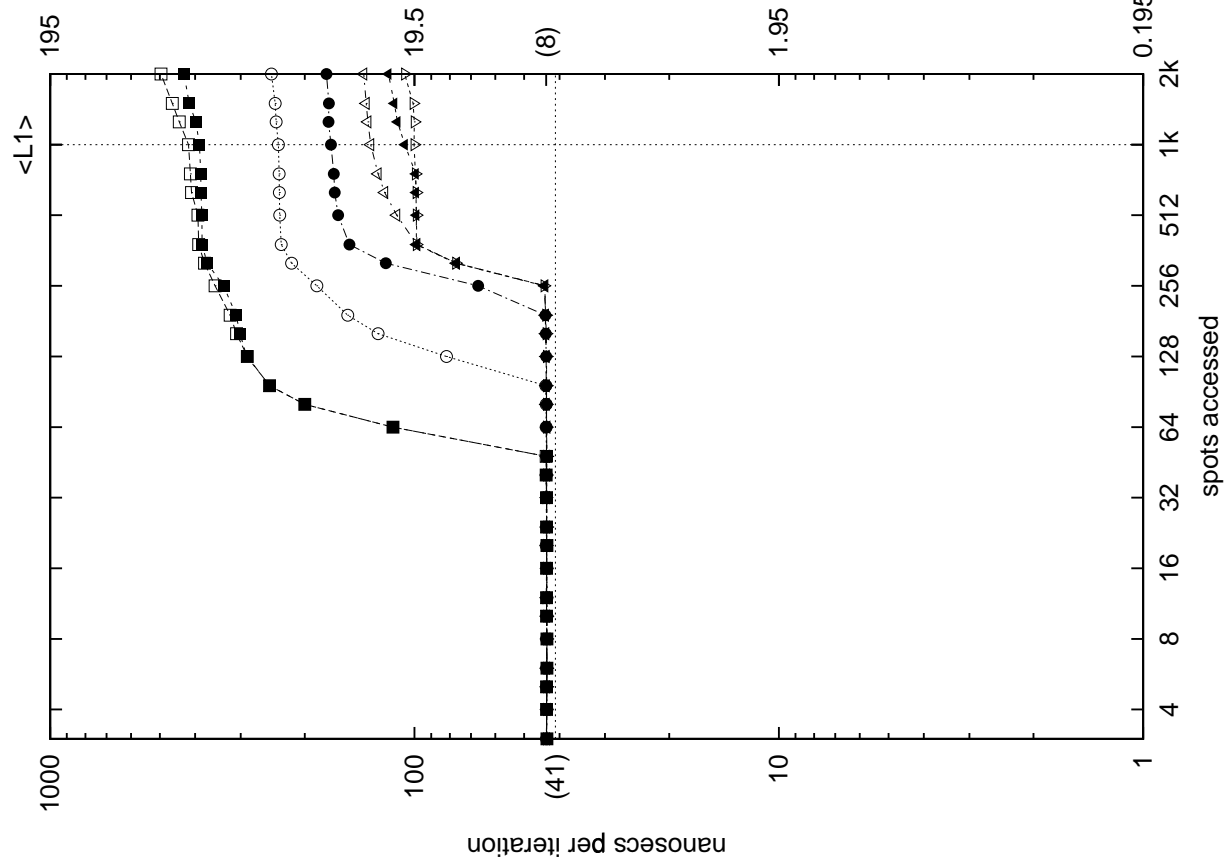


le120-195MHz-128MB.cache



stride: 256 * {128} {32} 8
 64 16 4

le120-195MHz-128MB.TLB



stride: 65664 32896 8320 2176
 16512 4224 1152