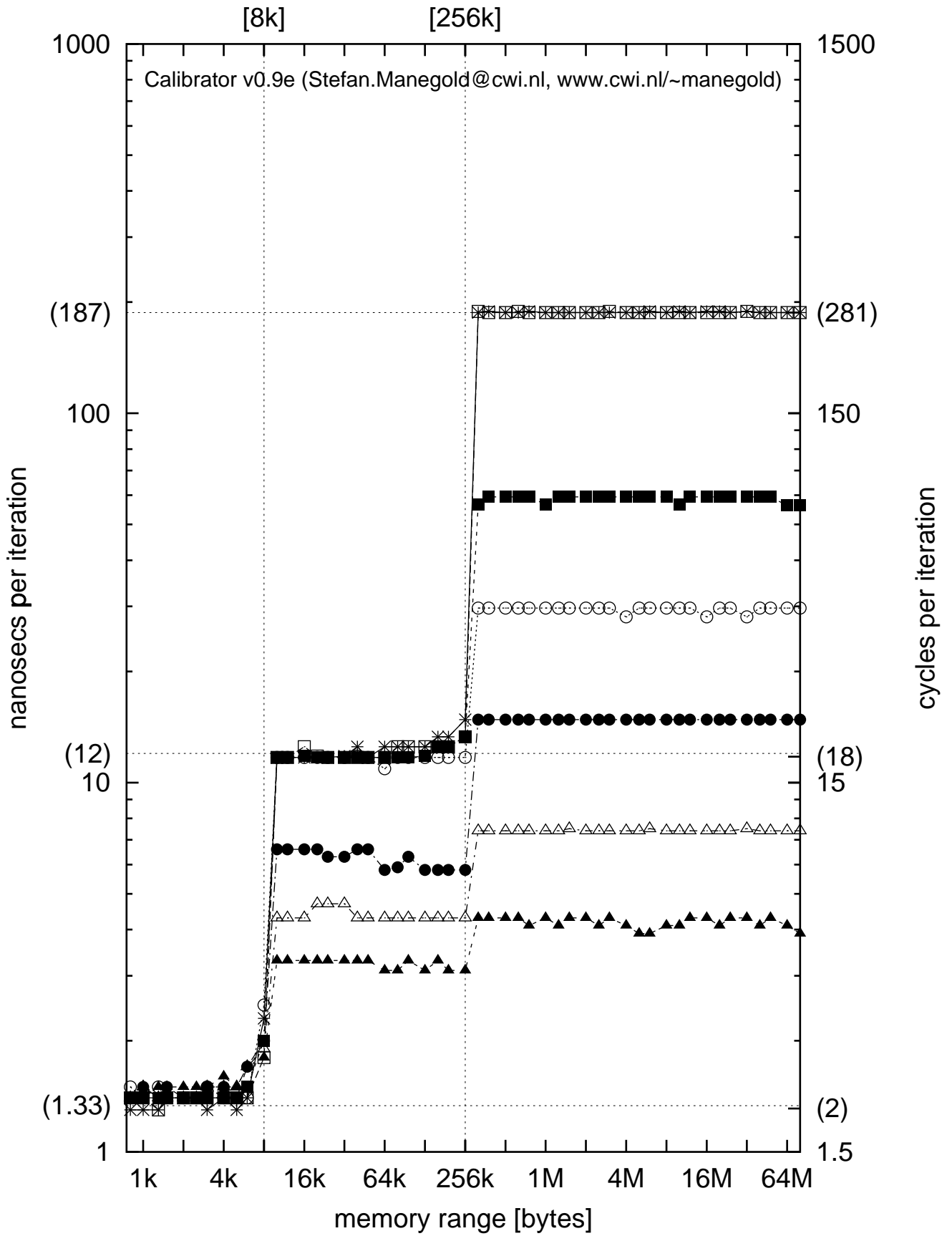


P4\_1500\_PC600\_Dual.cache-replace-time



1000

[8k]

[256k]

1500

Calibrator v0.9e (Stefan.Manegold@cwi.nl, www.cwi.nl/~manegold)

(187)

(281)

100

150

nanosecs per iteration

cycles per iteration

(12)

(18)

10

15

(1.33)

(2)

1

1.5

1k

4k

16k

64k

256k

1M

4M

16M

64M

memory range [bytes]

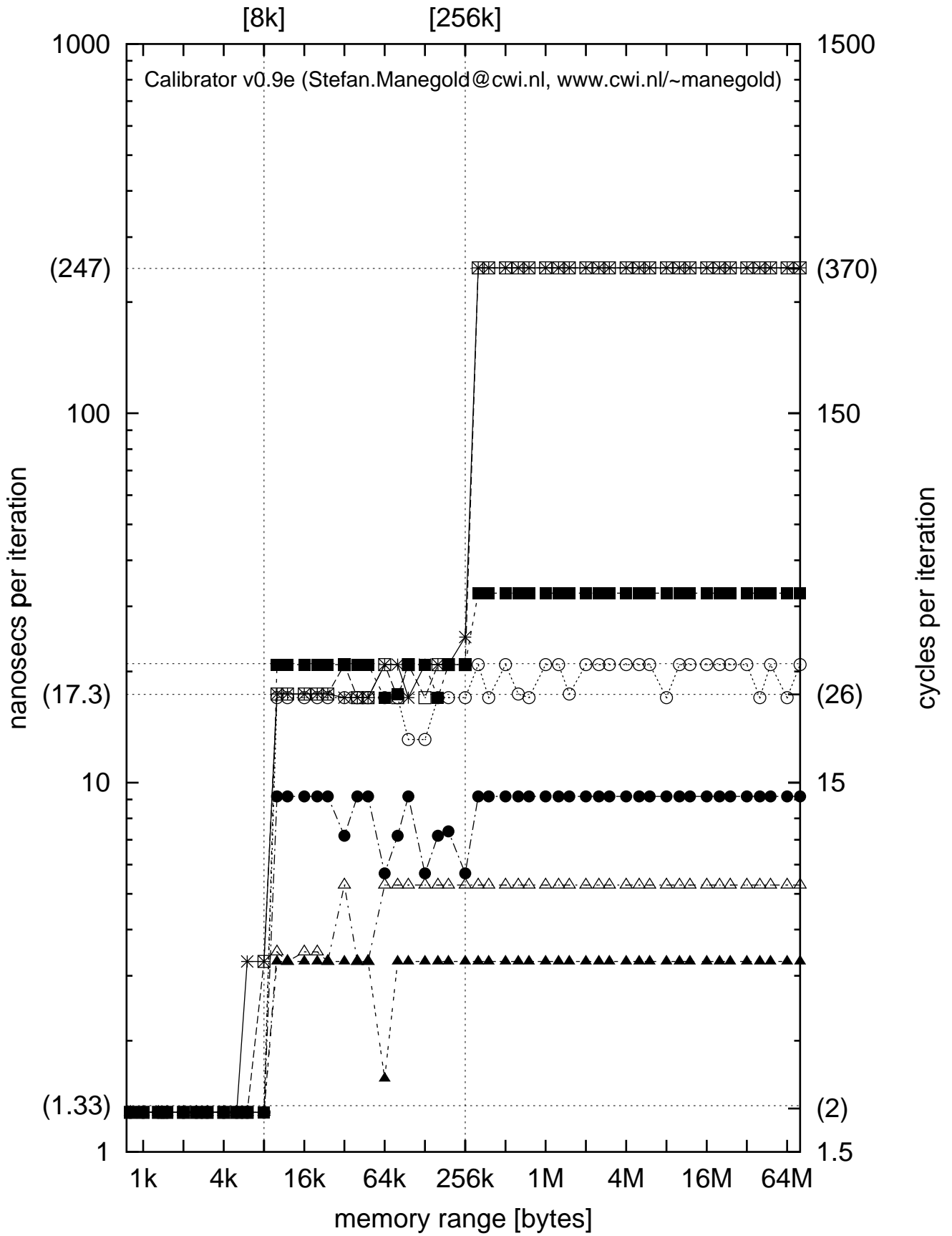
stride:  
256 —\*—

128 —□—  
64 —■—

32 —○—  
16 —●—

8 —△—  
4 —▲—

# P4\_1500\_PC600\_Dual.cache-miss-latency



(1.33)

(2)

(17.3)

(26)

(247)

(370)

[8k]

[256k]

1000

1500

100

150

10

15

1k 4k 16k 64k 256k 1M 4M 16M 64M

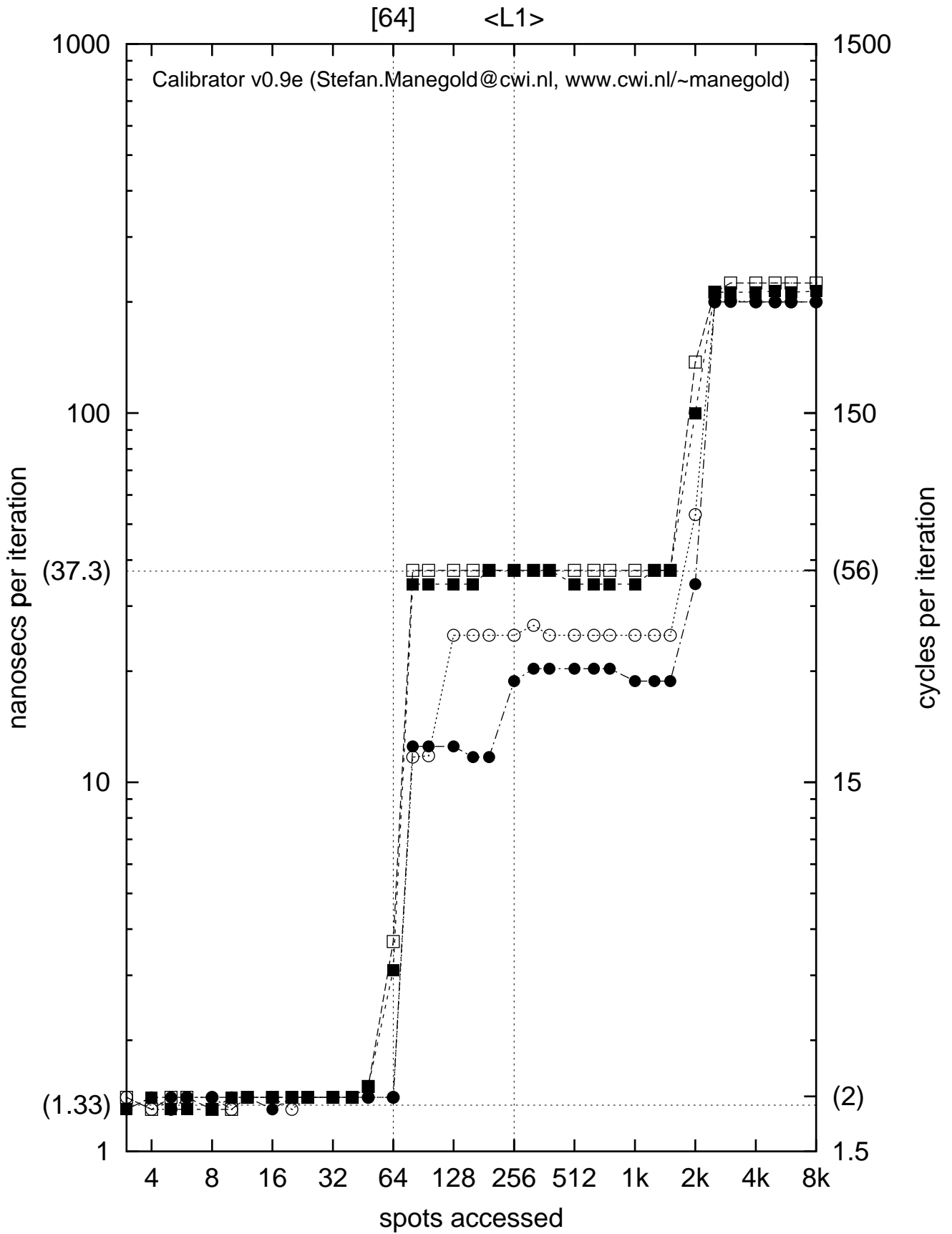
stride: 256 \*

128 □ 64 ■

32 ○ 16 ●

8 △ 4 ▲

# P4\_1500\_PC600\_Dual.TLB-miss-latency



stride: 8320 4224 1152  
 2176