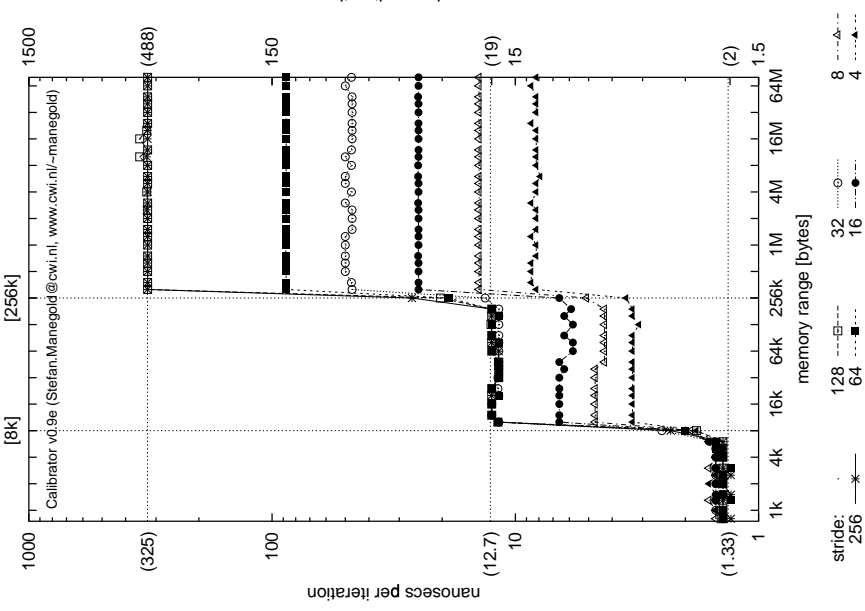
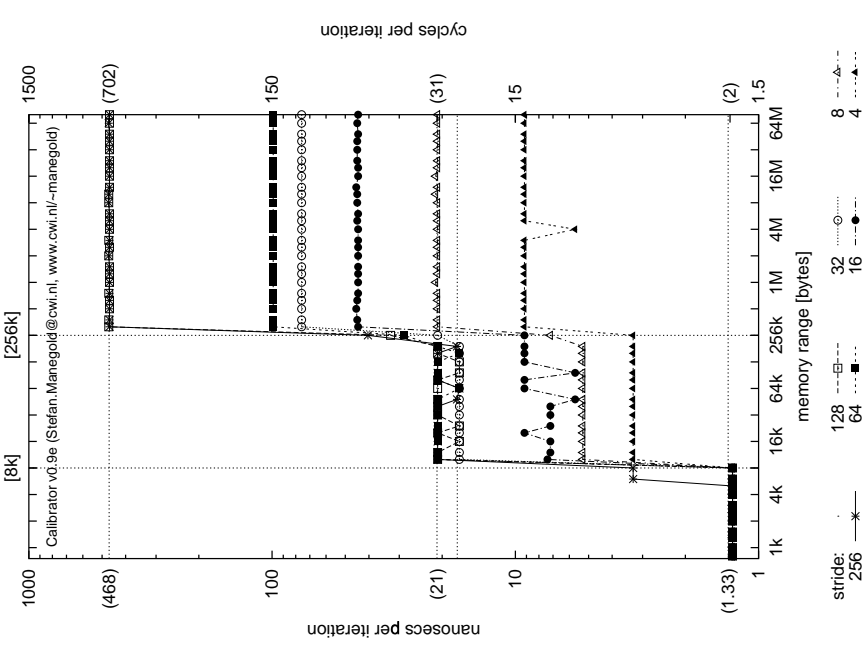


P4\_1500\_PC800\_Single.cache-replace-time



P4\_1500\_PC800\_Single.cache-miss-latency



P4\_1500\_PC800\_Single.TLB-miss-latency

