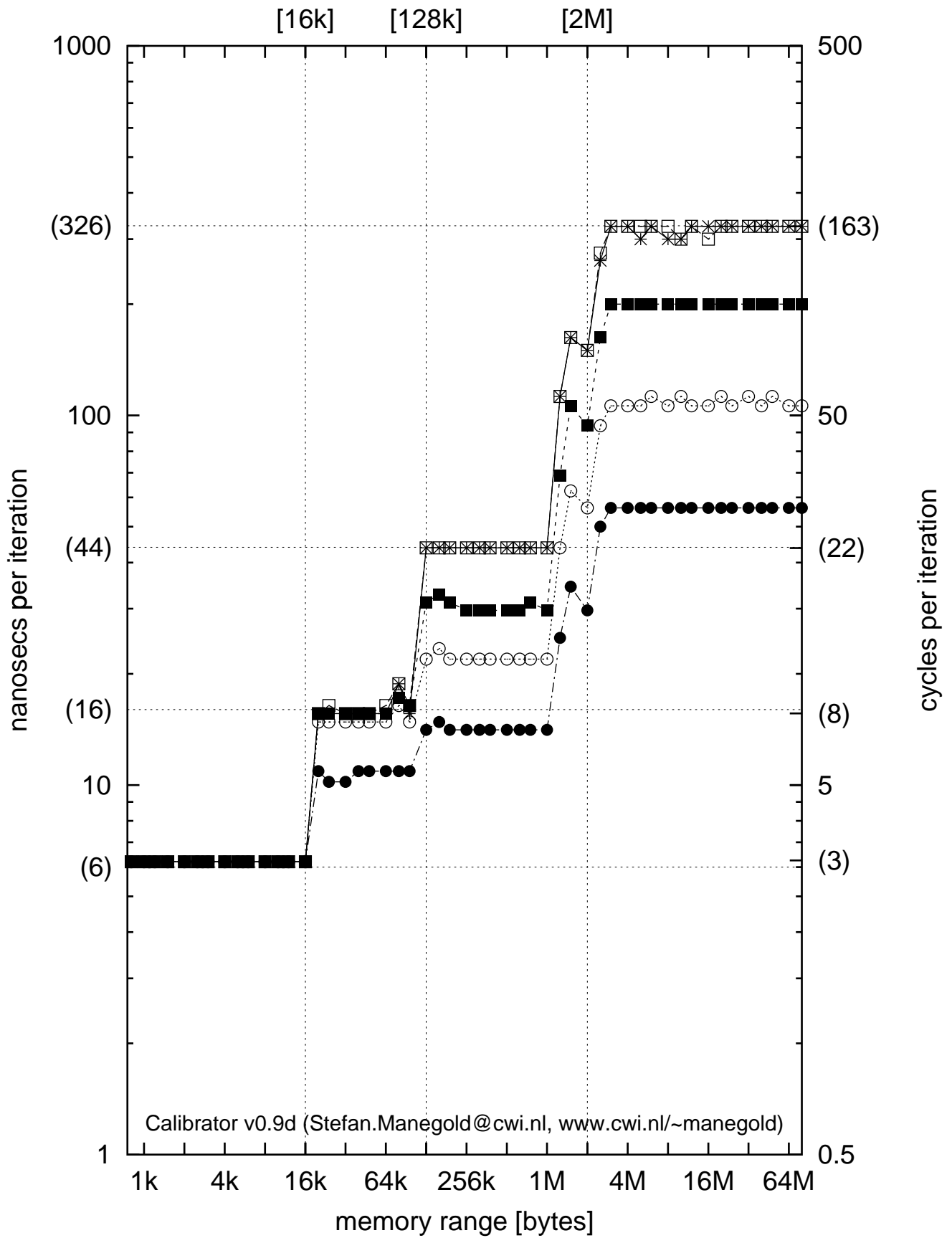


# ia-64.cache-replace-time

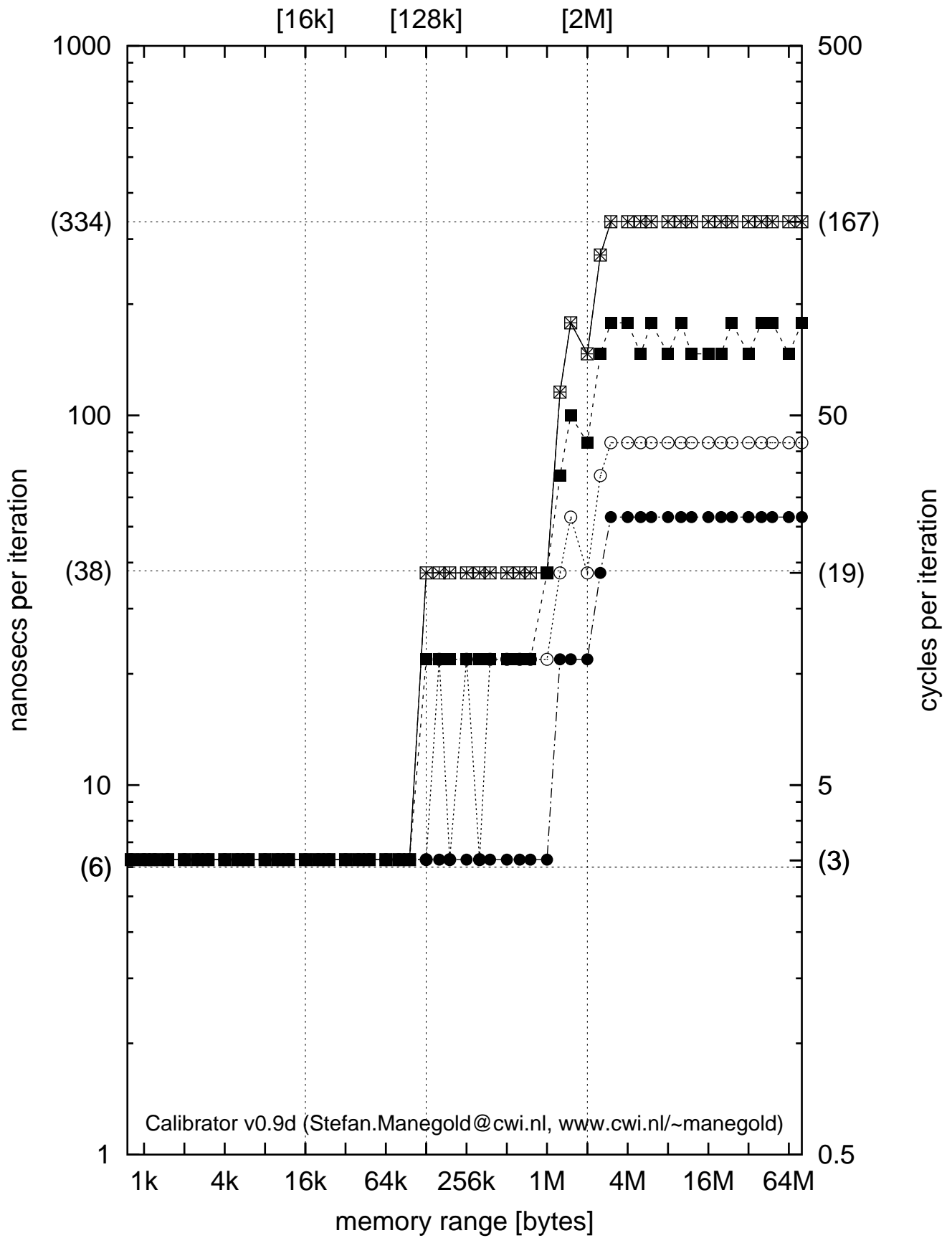


stride:  
128 —\*—

{64} —□—  
32 —■—

{16} —○—  
8 —●—

ia-64.cache-miss-latency

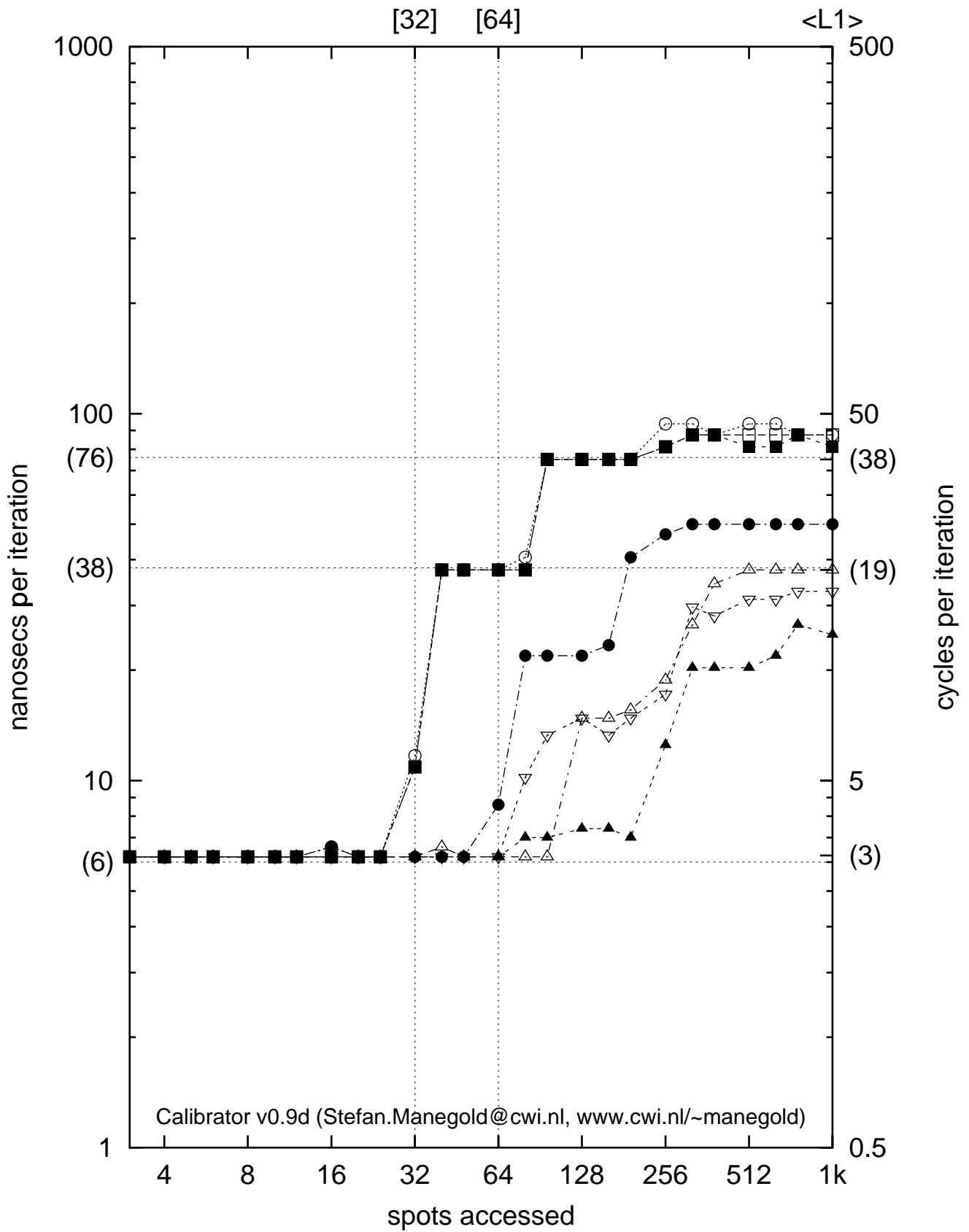


stride:  
128 —\*—

{64} ---□---  
32 ---■---

{16} ---○---  
8 ---●---

# ia-64.TLB-miss-latency



stride:		{16448}	-----○-----	2112	-----▲-----
65600	-----□-----	8256	-----●-----	1088	-----▽-----
32832	-----■-----	4160	-----△-----		