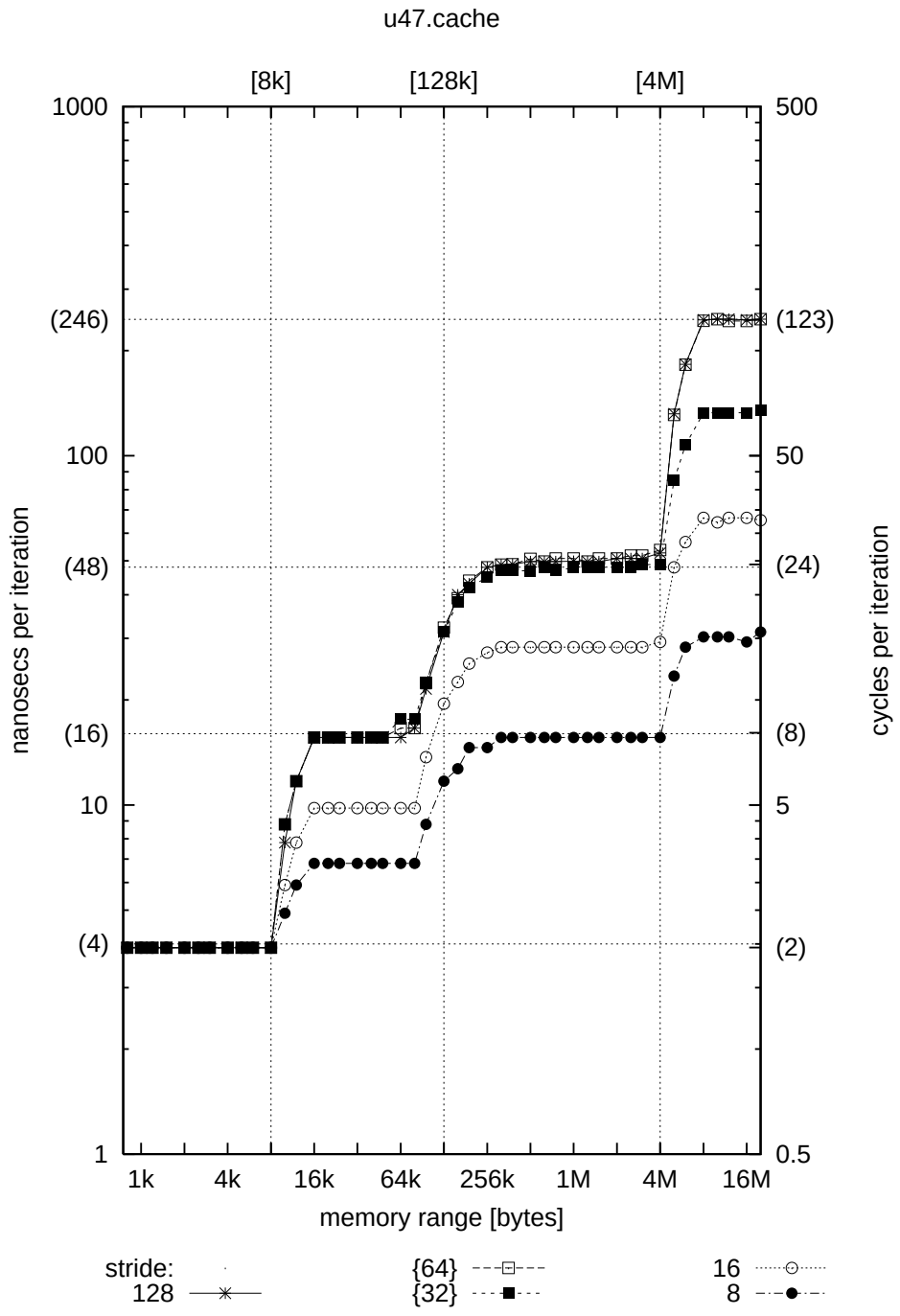
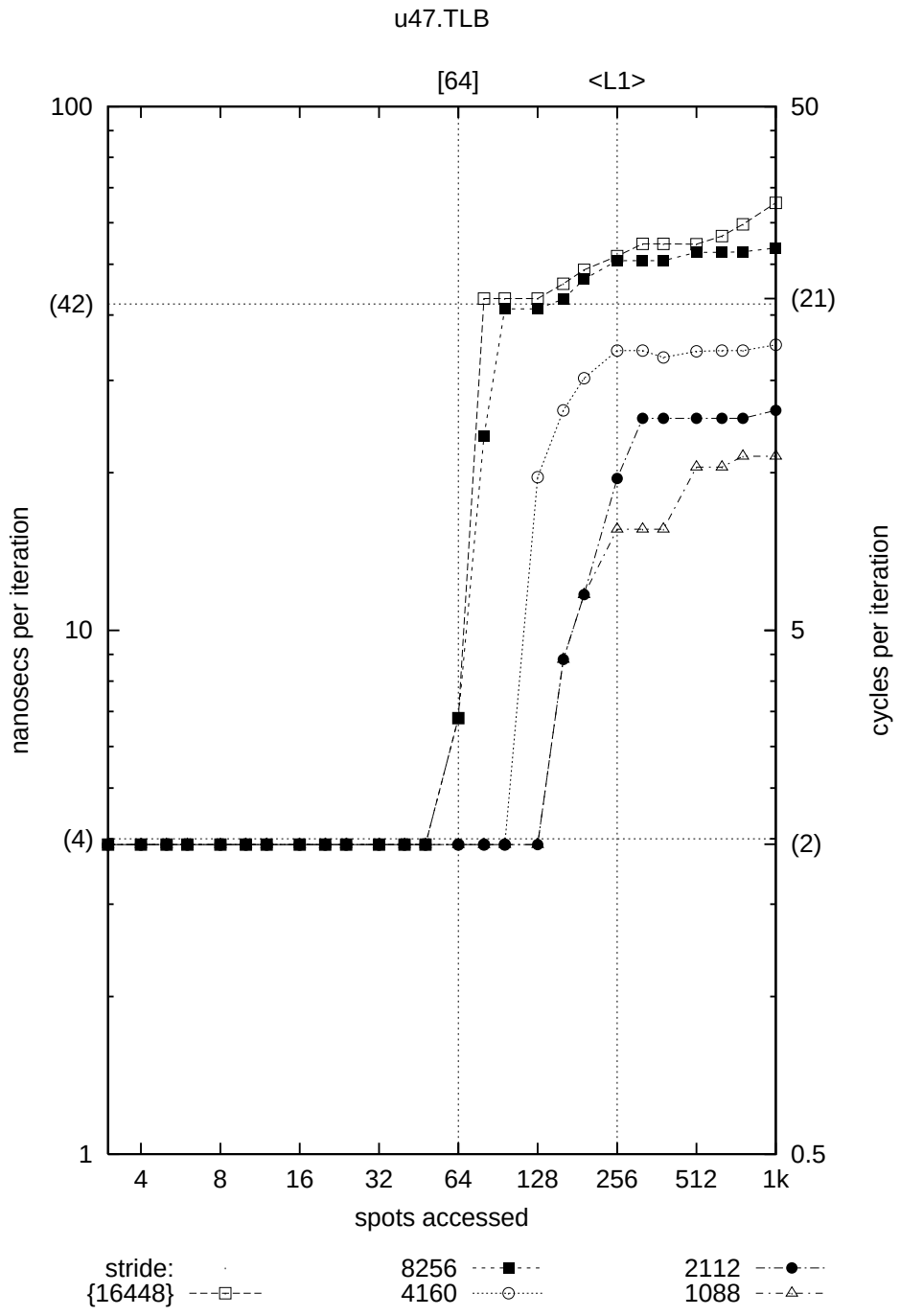

u47.cache
[8k]
[128k]
[4M]
1000
(246)
100
(48)
(16)
10
(4)
1
500
(123)
50
(24)
(8)
5
(2)
0.5
nanosecs per iteration
cycles per iteration
1k
4k
16k
64k
256k
1M
4M
16M
memory range [bytes]
stride:
128
{64}
{32}
16
8


u47.TLB
[64]
<L1>
100
(42)
10
(4)
1
50
(21)
5
(2)
0.5
nanosecs per iteration
cycles per iteration
4
8
16
32
64
128
256
512
1k
spots accessed
stride:
{16448}
8256
4160
2112
1088