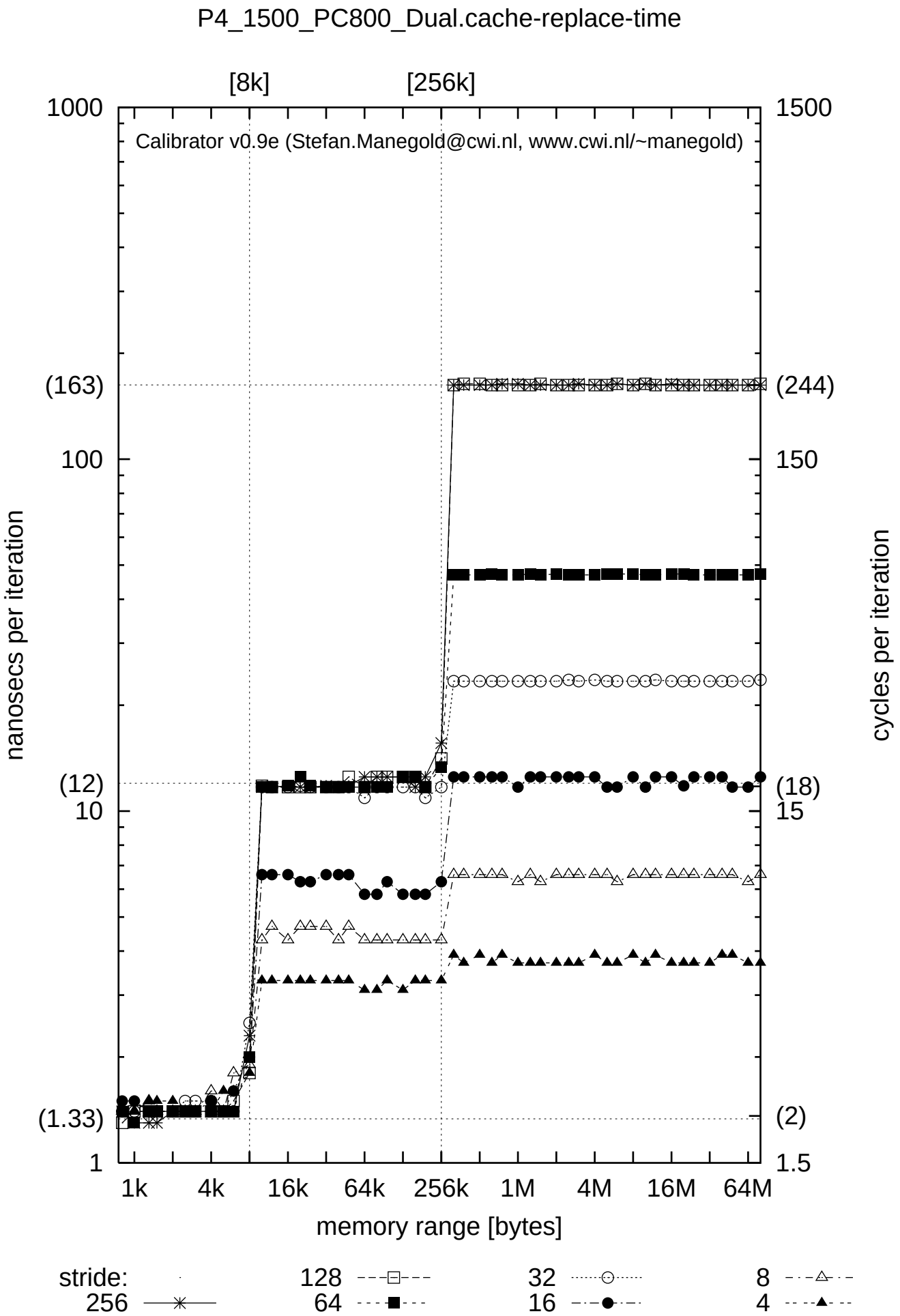
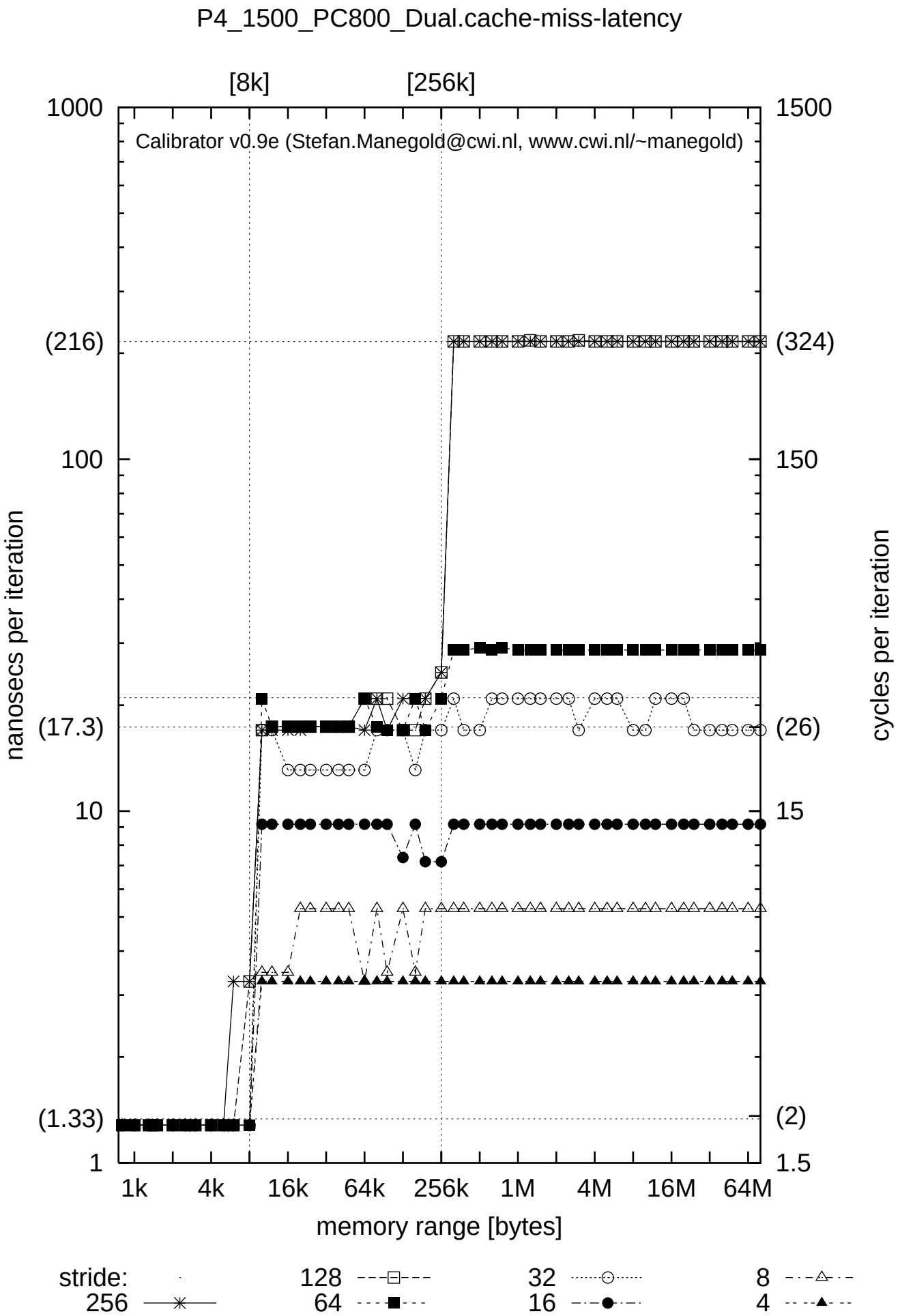
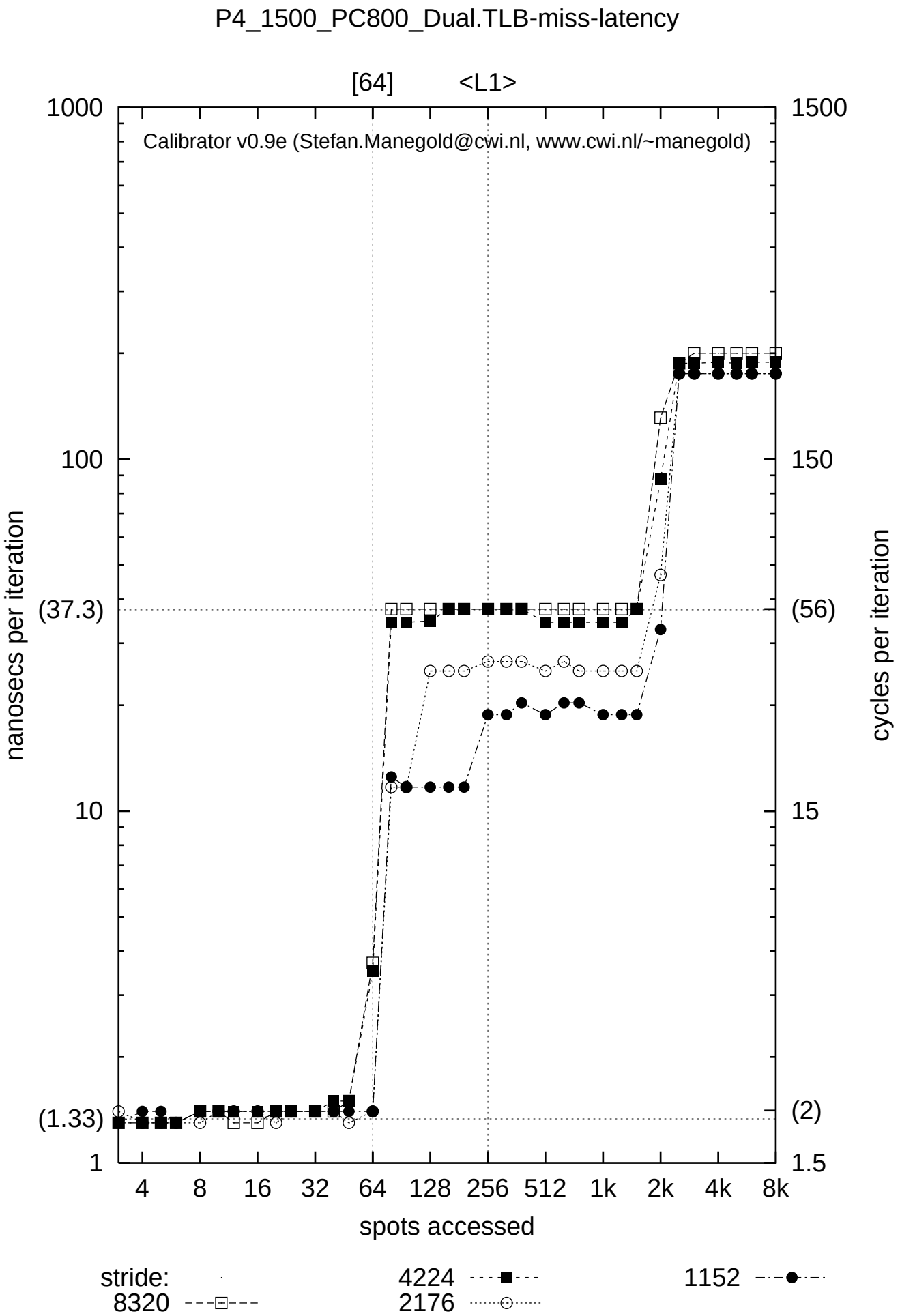

P4_1500_PC800_Dual.cache-replace-time
[8k]
[256k]
Calibrator v0.9e (Stefan.Manegold@cwi.nl, www.cwi.nl/~manegold)
1000
(163)
100
(12)
10
(1.33)
1
1500
(244)
150
(18)
15
(2)
1.5
nanosecs per iteration
cycles per iteration
1k
4k
16k
64k
256k
1M
4M
16M
64M
memory range [bytes]
stride:
256
128
64
32
16
8
4


P4_1500_PC800_Dual.cache-miss-latency
[8k]
[256k]
Calibrator v0.9e (Stefan.Manegold@cwi.nl, www.cwi.nl/~manegold)
1000
(216)
100
(17.3)
10
(1.33)
1
1500
(324)
150
(26)
15
(2)
1.5
nanosecs per iteration
cycles per iteration
1k
4k
16k
64k
256k
1M
4M
16M
64M
memory range [bytes]
stride:
256
128
64
32
16
8
4


P4_1500_PC800_Dual.TLB-miss-latency
[64] <L1>
Calibrator v0.9e (Stefan.Manegold@cwi.nl, www.cwi.nl/~manegold)
1000
100
(37.3)
10
(1.33)
1
1500
150
(56)
15
(2)
1.5
nanosecs per iteration
cycles per iteration
4
8
16
32
64
128
256
512
1k
2k
4k
8k
spots accessed
stride:
8320
4224
2176
1152