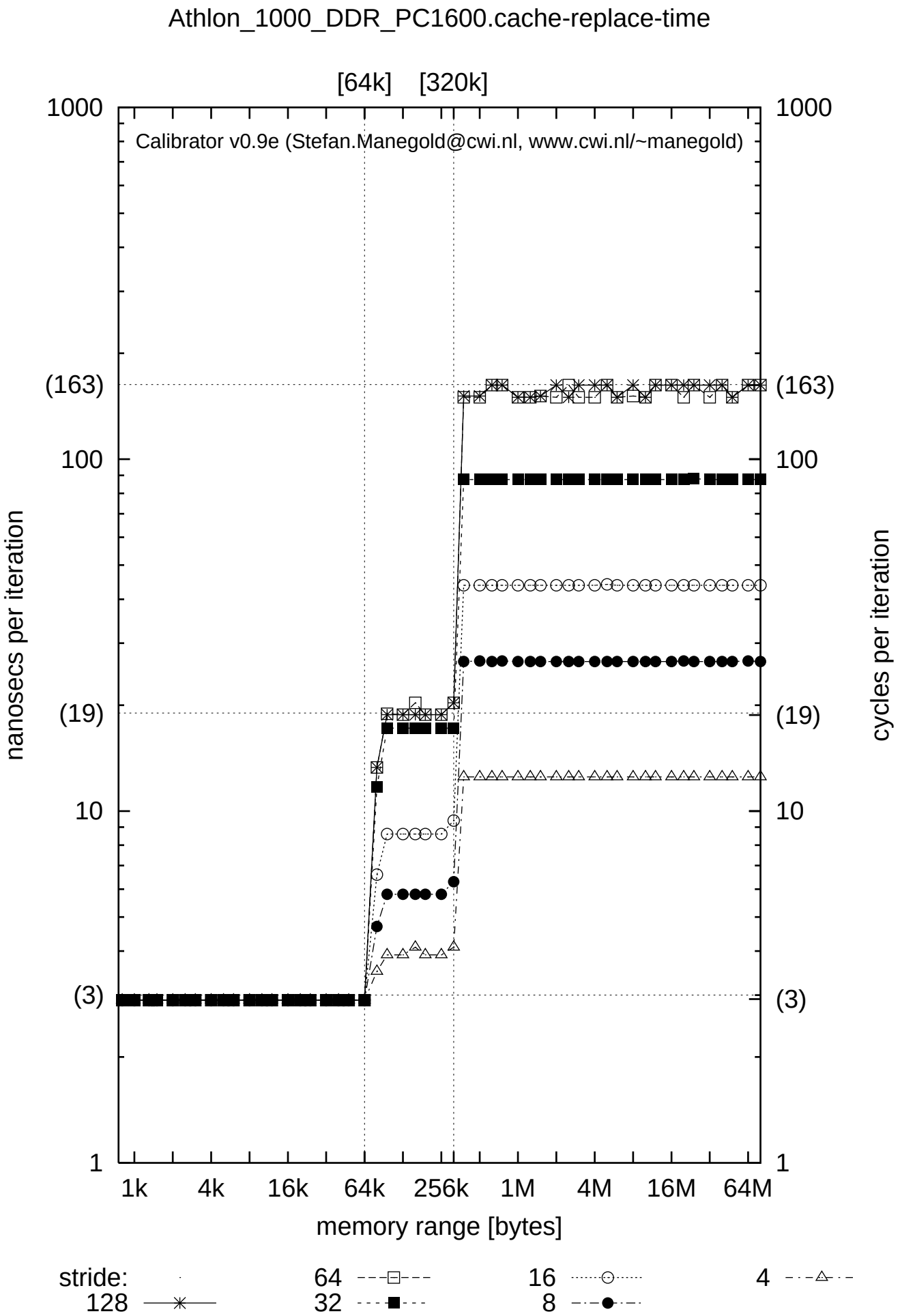
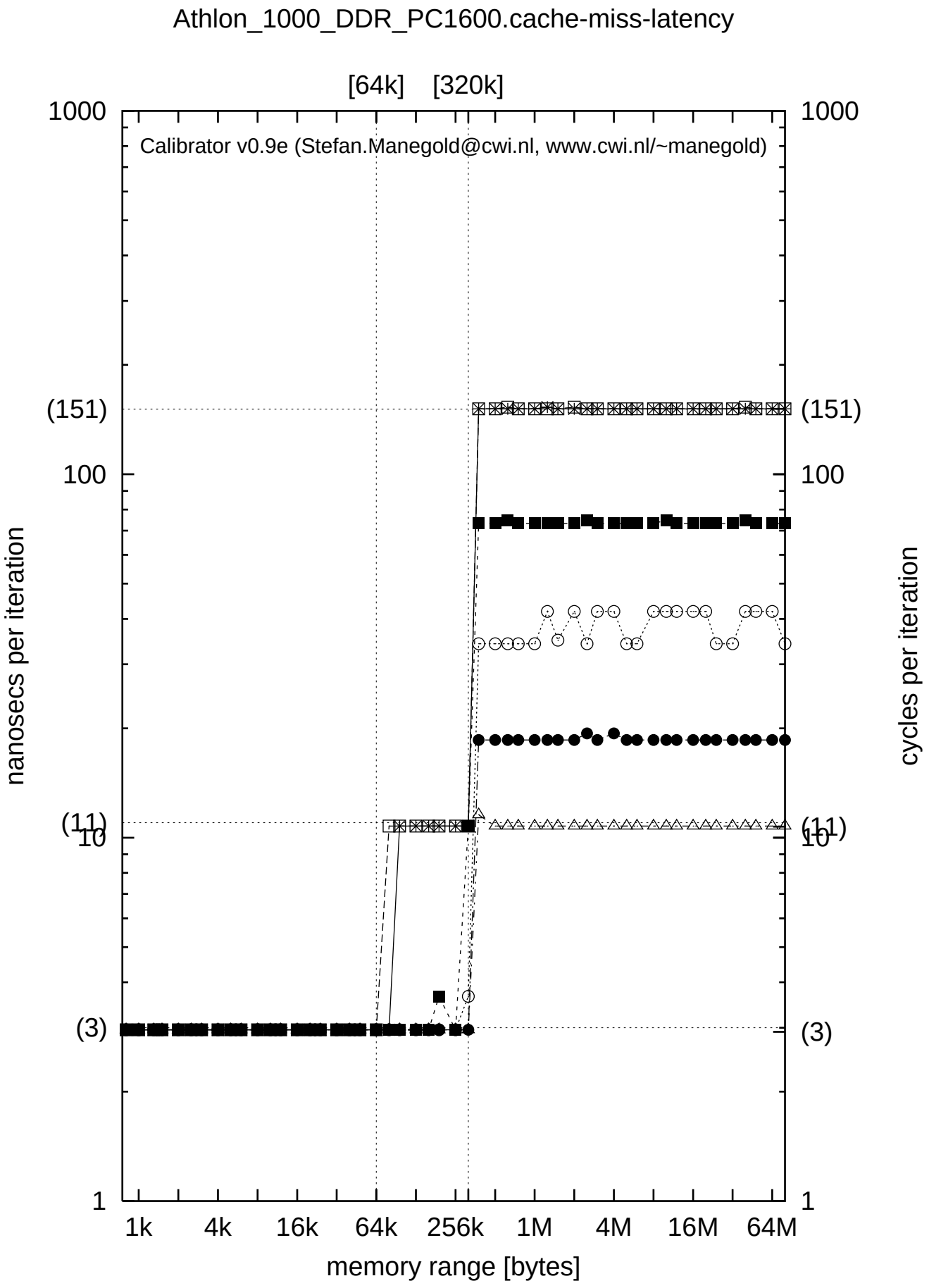
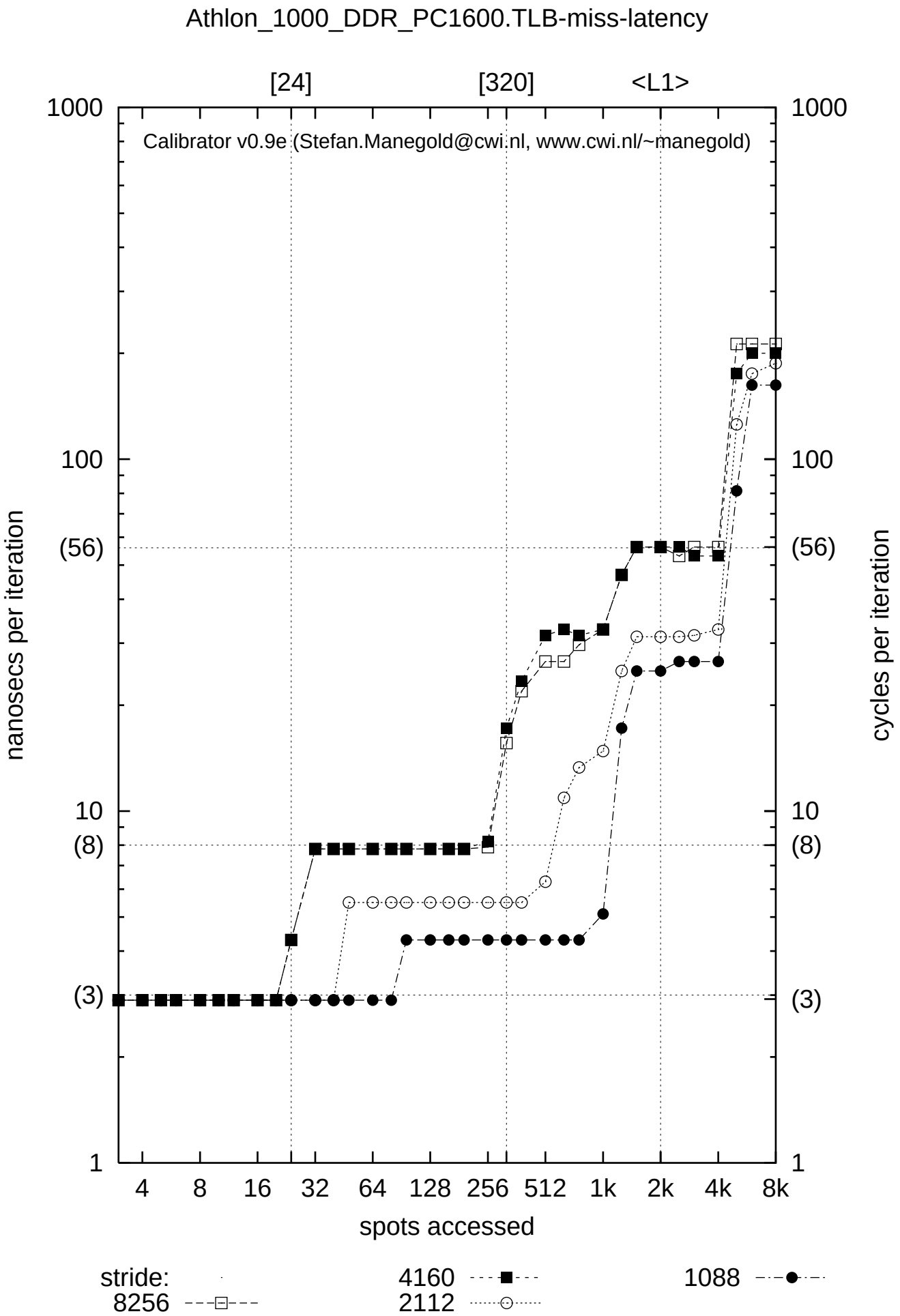

Athlon_1000_DDR_PC1600.cache-replace-time
[64k] [320k]
Calibrator v0.9e (Stefan.Manegold@cwi.nl, www.cwi.nl/~manegold)
1000
(163)
100
(19)
10
(3)
1
nanosecs per iteration
cycles per iteration
1k
4k
16k
64k
256k
1M
4M
16M
64M
memory range [bytes]
stride:
128
64
32
16
8
4


Athlon_1000_DDR_PC1600.cache-miss-latency
[64k] [320k]
Calibrator v0.9e (Stefan.Manegold@cwi.nl, www.cwi.nl/~manegold)
1000
(151)
100
(11)
10
(3)
1
nanosecs per iteration
cycles per iteration
1k
4k
16k
64k
256k
1M
4M
16M
64M
memory range [bytes]
stride:
128
64
32
16
8
4


Athlon_1000_DDR_PC1600.TLB-miss-latency
[24]
[320]
<L1>
Calibrator v0.9e (Stefan.Manegold@cwi.nl, www.cwi.nl/~manegold)
1000
100
(56)
10
(8)
(3)
1
nanosecs per iteration
cycles per iteration
4
8
16
32
64
128
256
512
1k
2k
4k
8k
spots accessed
stride:
4160
1088
8256
2112